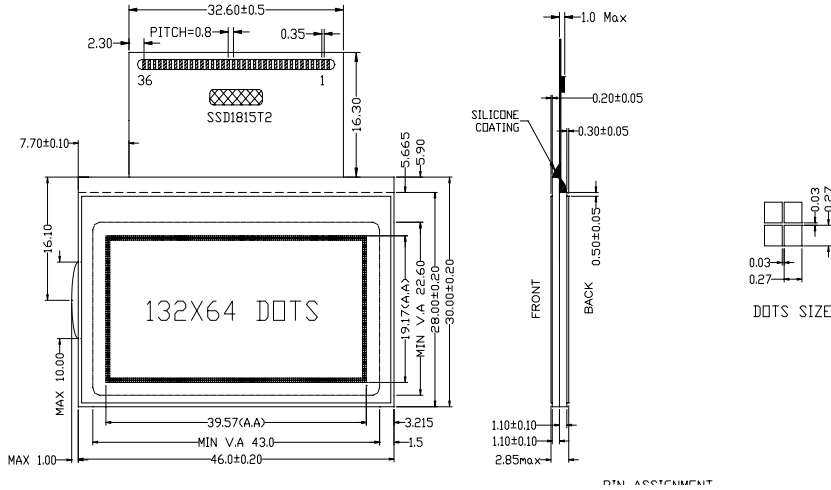




*LCM Module EMTG13264B Series*

**6.Physical Dimension**



Note:

1. All dimension are in mm.
2. Glass thickness: Front:1.1 Back: 1.1
3. Duty: 1/65 Bias: 1/9 Vop: 10V
4. STN, Yellow-Green  
Positive, Transflective Mode.
5. Graph, 132\*64 Dots
6. Operating Temperature:  
0deg~+50deg;  
Storage Temperature:  
-10deg~+600deg;
7. Viewing direction: 12 o'clock.
8. Module type: TAB

**7. Mechanical Data**

ITEM	SPECIFICATION	UNIT
Module Size(W*H*L)	46*46.3*2.85	mm
View Area(W*H)	43*22.6	mm
Active Area(W*H)	39.57*19.17	mm
Number of Dot	128*64	dots
Dot Pitch(W*H)	0.30*0.30	mm
Dot Size(W*H)	0.27*0.27	mm

**8. PIN Configuration**

ITEM	SYMBOL	DESCRIPTION
1	Dummy	Dummy pin
2	CL	Display clock input/output.
3	/CS1	Chip select inputs
4	/RES	Reset signal input
5	D/C	Data / Command control
6	R/W	6800-MCU, Use as read/write select input, Read "H", Write "L"; 8080-MCU, Use as write select input when "L".
7	E	6800-MCU, Use as read/write enable signal. Read/Write operation is initiated when Enable signal is "H"; 8080-MCU, Use as read select input when "L".
8~15	D0~D7	Parallel mode, Display data signal; Serial mode, D7 is the serial data input, and D6 is the serial clock input.
16	V <sub>DD</sub>	Supply voltage for logic circuit
17	V <sub>SS</sub>	Ground
18	V <sub>EE</sub>	Most negative voltage for logic circuit
19	C3N	When internal DC-DC voltage converter is used, external capacitor(s) is/are connected between these pins.
20	C1P	
21	C2N	
22	C2P	
23	VFS	Provide an external voltage reference for the internal voltage regulator.
24	VL2	LCD driving voltage levels. All these levels are referenced to V <sub>DD</sub> (V <sub>DD</sub> > VL2 > VL3 > VL4 > VL5 > VL6)
25	VL3	
26	VL4	
27	VL5	
28	VL6	
30	VF	Built-in voltage regulator for generating VL6
31	CLS	Internal clock enable when it is "H"
32	C68/80	MCU parallel interface selection input. 6800 when it is "H", 8080 when it is "L"
33	P/S	Serial/parallel interface selection input. Parallel when it is "H", Serial when it is "L"
34	/HPM	Control input of High Power Current Mode
35	IRS	Enable the internal resistors network for the voltage regulator
36	Dummy	Dummy pin